

PreProLAr1 v2.2.1, Target: VCU118, Date: 1/20/2025

Component Name	Utilization FFs	Utilization LUTs	Description
adder2x1_sub0123	9	311	Decoder
adder4x1_sub01	14	211	Decoder
adder4x1_sub23	6	5	Decoder
adder4x1_sub45	11	235	Decoder
adder4x1_sub67	11	235	Decoder
adder16x1_sub0	43	130	Decoder
adder16x1_sub1	41	31	Decoder
adder16x1_sub2	44	149	Decoder
adder16x1_sub3	41	31	Decoder
adder16x1_sub4	43	148	Decoder
adder16x1_sub5	41	31	Decoder
adder16x1_sub6	43	165	Decoder
adder16x1_sub7	43	165	Decoder
bitonic_sorter_sub0	859	897	Decoder
bitonic_sorter_sub1	1024	1353	Decoder
bitonic_sorter_sub2	1018	1082	Decoder
bitonic_sorter_sub3	1184	1523	Decoder
bitonic_sorter_sub4	1018	1020	Decoder
bitonic_sorter_sub5	1184	1523	Decoder
bitonic_sorter_sub6	1177	1193	Decoder
bitonic_sorter_sub7	1344	1692	Decoder
cellTowExt	18548	1957	Cells assignments
energyTowExt/ energyTowSum0 energyTowSum1	5303 5308	7049 7249	Summing Engines
mask2s_index	259	736	Cell local index
mux16to32_sub01	139	15	Decoder
mux16to32_sub23	170	425	Decoder
mux16to32_sub45	170	17	Decoder
mux16to32_sub67	201	512	Decoder
mux32to64_sub01	178	14	Decoder
mux32to64_sub23	212	491	Decoder
mux64to128_sub01	212	68	Decoder

X0Y0	X0Y1	X0Y2	X0Y3	X0Y4	X0Y5	X0Y6	X0Y7	X0Y8	X0Y9	X0Y10	X0Y11	X0Y12	X0Y13	X0Y14
X1Y0	X1Y1	X1Y2	X1Y3	X1Y4	X1Y5	X1Y6	X1Y7	X1Y8	X1Y9	X1Y10	X1Y11	X1Y12	X1Y13	X1Y14
X2Y0	X2Y1	X2Y2	X2Y3	X2Y4	X2Y5	X2Y6	X2Y7	X2Y8	X2Y9	X2Y10	X2Y11	X2Y12	X2Y13	X2Y14
X3Y0	X3Y1	X3Y2	X3Y3	X3Y4	X3Y5	X3Y6	X3Y7	X3Y8	X3Y9	X3Y10	X3Y11	X3Y12	X3Y13	X3Y14
X4Y0	X4Y1	X4Y2	X4Y3	X4Y4	X4Y5	X4Y6	X4Y7	X4Y8	X4Y9	X4Y10	X4Y11	X4Y12	X4Y13	X4Y14
X5Y0	X5Y1	X5Y2	X5Y3	X5Y4	X5Y5	X5Y6	X5Y7	X5Y8	X5Y9	X5Y10	X5Y11	X5Y12	X5Y13	X5Y14