

PreProLAr1 v3.0.0, Target: VPK180, Date: 1/16/2025

Component Name	Utilization FFs	Utilization LUTs	Description
adder2x1_sub0123	9	200	Decoder
adder4x1_sub01	9	130	Decoder
adder4x1_sub23	6	5	Decoder
adder4x1_sub45	8	138	Decoder
adder4x1_sub67	8	138	Decoder
adder16x1_sub0	41	43	Decoder
adder16x1_sub1	41	31	Decoder
adder16x1_sub2	41	43	Decoder
adder16x1_sub3	41	31	Decoder
adder16x1_sub4	41	43	Decoder
adder16x1_sub5	41	31	Decoder
adder16x1_sub6	41	43	Decoder
adder16x1_sub7	41	43	Decoder
bitonic_sorter_sub0	899	1643	Decoder
bitonic_sorter_sub1	1060	1703	Decoder
bitonic_sorter_sub2	1058	1684	Decoder
bitonic_sorter_sub3	1220	1798	Decoder
bitonic_sorter_sub4	1058	1748	Decoder
bitonic_sorter_sub5	1220	1780	Decoder
bitonic_sorter_sub6	1217	1798	Decoder
bitonic_sorter_sub7	1380	1863	Decoder
cellTowExt	15596	192	Cells assignments
energyTowExt/ energyTowSum0 energyTowSum1	4030 4030	7904 7904	Summing Engines
mask2s_index	258	1536	Cell local index
mux16to32_sub01	140	174	Decoder
mux16to32_sub23	187	598	Decoder
mux16to32_sub45	187	240	Decoder
mux16to32_sub67	234	710	Decoder
mux32to64_sub01	128	0	Decoder
mux32to64_sub23	136	454	Decoder
mux64to128_sub01	136	68	Decoder

X0Y1	X0Y2	X0Y3	X0Y4	X0Y5	X0Y6	X0Y7	X0Y8	X0Y9	X0Y10	X0Y11	X0Y12	X0Y13
X1Y1	X1Y2	X1Y3	X1Y4	X1Y5	X1Y6	X1Y7	X1Y8	X1Y9	X1Y10	X1Y11	X1Y12	X1Y13
X2Y1	X2Y2	X2Y3	X2Y4	X2Y5	X2Y6	X2Y7	X2Y8	X2Y9	X2Y10	X2Y11	X2Y12	X2Y13
X3Y1	X3Y2	X3Y3	X3Y4	X3Y5	X3Y6	X3Y7	X3Y8	X3Y9	X3Y10	X3Y11	X3Y12	X3Y13
X4Y1	X4Y2	X4Y3	X4Y4	X4Y5	X4Y6	X4Y7	X4Y8	X4Y9	X4Y10	X4Y11	X4Y12	X4Y13
X5Y1	X5Y2	X5Y3	X5Y4	X5Y5	X5Y6	X5Y7	X5Y8	X5Y9	X5Y10	X5Y11	X5Y12	X5Y13
X6Y1	X6Y2	X6Y3	X6Y4	X6Y5	X6Y6	X6Y7	X6Y8	X6Y9	X6Y10	X6Y11	X6Y12	X6Y13
X7Y1	X7Y2	X7Y3	X7Y4	X7Y5	X7Y6	X7Y7	X7Y8	X7Y9	X7Y10	X7Y11	X7Y12	X7Y13
X8Y1	X8Y2	X8Y3	X8Y4	X8Y5	X8Y6	X8Y7	X8Y8	X8Y9	X8Y10	X8Y11	X8Y12	X8Y13
X9Y1	X9Y2	X9Y3	X9Y4	X9Y5	X9Y6	X9Y7	X9Y8	X9Y9	X9Y10	X9Y11	X9Y12	X9Y13